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Docket No. 10001.000600 (NVLS 379)  
Response To Final Rejection  
July 1, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Craig L. Stevens et al.

Serial No. 09/872,796

Examiner: Erik J. Kielin

Filing Date: 06/01/2001

Art Unit: 2813

Title: Architecture For High Throughput Semiconductor Processing Applications

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Honorable Commissioner of Patents  
Washington, D.C. 20231

RESPONSE TO FINAL REJECTION

INTRODUCTORY COMMENTS

Sir:

This paper is responsive to the office action mailed on 05/19/2003 (hereinafter the "last office action").

Please amend the above-identified application as follows:

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**Okamoto &  
Benedicto LLP**

# Fax

<b>To:</b>	Examiner Eric J. Kielin USPTO	<b>From:</b>	Patrick D. Benedicto
<b>Fax:</b>	(703) 746-3877	<b>Pages:</b>	8 (including fax cover sheet)
<b>Phone:</b>		<b>Date:</b>	July 1, 2003
<b>Re:</b>	Response to Final Rejection of Application No. 09/872,796		
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Response To Final Rejection (6 pages)

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